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(Use as many sheets as necessary)

Sheet 1 of 2

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Complete if Known				
Application Number	10/684,057			
Filing Date	October 9, 2003			
First Named Inventor	John W. Rapp			
Art Unit	2818			
Examiner Name				
Attorney Docket Number	1934-14-3			

	U.S. PATENT DOCUMENTS				
Examiner (Cite	Document Number	Publication Date MM-OD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Releva
tnitials *	No.1	Number • Kind Code ² (if known)		Gild Scallen	Passages or Relevant Figures Appear
/RBF/		US-6,704,816 B1	03/09/2004	Burke	
		UD-2004/101601-A1	00/46/2004	Mathus, et al	
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Examiner Initials*	Cite No.1	Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	T⁵
						

Examiner Signature	/Richard Franklin/	Date Considered	07/27/2007

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Substitute for form 14498/PTO				Complete if Known		
	D14 4 T1	AN DIA	01 00UDE	Application Number	10/684,057	
INFORMATION DISCLOSURE				Filing Date	October 9, 2003	
STATEMENT BY APPLICANT		First Named Inventor	John W. Rapp			
				Art Unit	2818	
(Use as many sheets as necessary)		Examiner Name				
Sheet	2	of	2	Attorney Docket Number	1934-14-3	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T 2
/RBF/		International Search Report for PCT/US2005/035818 dated August 4, 2006	
/			
Evaminer		I Date	

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form 1449B/PTO MFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known Application Number 10/684,057 Filing Date October 9, 2005 First Named Inventor John Rapp Art Unit 2181 Richard B. Franklin Examiner Name

(Use as many sheets as necessary)

Attorney Docket Number of 1 Sheet 1934-14-3

	NON PATENT LITERATURE DOCUMENTS	,
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	BAKSHI S; GAJSKI D D; "Partitioning and Pipelining for Performance-Constrained Hardware/Software Systems", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 7, NR. 4, PG. 419-432, (1999-12-00), XP000869229	
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		Cite No.¹ Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. BAKSHI S; GAJSKI D D; "Partitioning and Pipelining for Performance-Constrained Hardware/Software Systems", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 7, NR. 4, PG. 419-432, (1999-12-00),

Examiner Signature	/Richard Franklin	Date Considered	07/27/2007

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